



SRI KRISHNADEVARAYA EDUCATIONAL TRUST
SIR M. VISVESVARAYA INSTITUTE OF TECHNOLOGY

(Affiliated to VTU-Belagavi, Recognized by AICTE and Accredited by NAAC)
Krishnadevarayanagar, Off International Airport Road, Hunasamaranahalli, Bengaluru – 562 157



DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION
ENGINEERING

Organized

Poster Presentation of ideas

In association with

INSTITUTION INNOVATION COUNCIL (IIC)

Description:

The poster presentation competition was held at Analog Electronics Lab on November 23, 2023. The competition aimed to provide a platform for students to showcase their research and innovative ideas in various fields of study. A total of 30 students from different departments participated in the competition. The presentations were conducted interactively, allowing students to ask questions and engage with the presenters. A panel of three judges, consisting of faculty members, evaluated the posters and presentations. The winners of the competition were announced based on the judges' scores.

Objectives:

- The competition aimed to encourage students to think innovatively and come up with creative solutions to real-world problems.
- Students were able to improve their presentation skills by effectively communicating their ideas and presenting information in a clear and concise manner.

Benefits in terms of learning/skills/knowledge obtained:

- The competition helped students develop a range of skills, including research, critical thinking, communication and time management skills.
- Improves the student's ability to interpret and create visual information.
- Enriching the innovative ideas of the student.

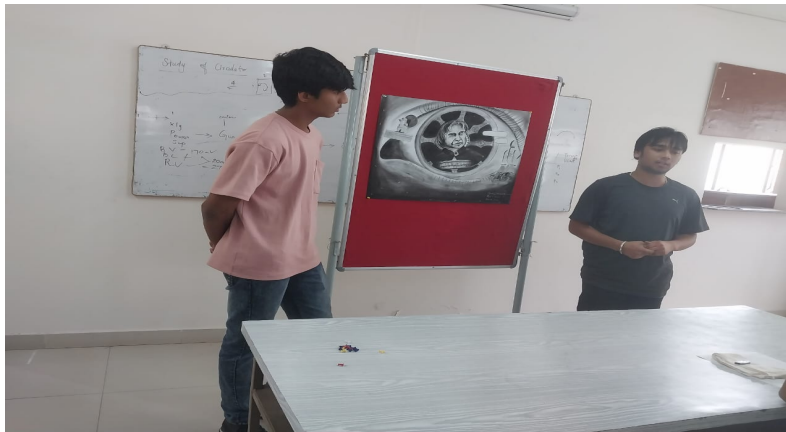
Resource Person/Judge details



Professor and HOD, Department of ETE,
SirMVIT, Bengaluru

Dr. E. Kavitha is currently working as a Professor and HOD in the Department of Electronics and Telecommunication Engineering at SirMVIT, Bengaluru. She is an Innovation Ambassador under MoE's Innovation Cell & AICTE, and she received the Bharath Vidya Ratan award in 2018. Dr. E.Kavitha has published over 50 articles in SCI, SCOPUS, and other indexed journals and conferences. Her research interests include Communication networks, Wireless sensor networks, Image processing, Data Science and Machine Learning.

Related Photographs of the Event



Poster of the Event:

The poster is for an event organized by the Department of Electronics and Telecommunication Engineering at Sir M. Visvesvaraya Institute of Technology. It features logos for the institution, MoE's Innovation Cell, and the Institution's Innovation Council. The event is titled 'Poster Presentation of Ideas' and is scheduled for November 23, 2023, at the Analog Electronics Lab (306). An illustration shows a person presenting a poster. The organizing committee includes Prof. Rakesh S. G. as Chair, Dr. Vanipriya Ch. as President, Dr. Sasmita Mohapatra as Vice President, and Dr. Soumya Patil as Convenor.

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MoE's
INNOVATION CELL
(GOVERNMENT OF INDIA)

**INSTITUTION'S
INNOVATION
COUNCIL**
OFFICE OF INNOVATION & ENTREPRENEURSHIP

**Department of Electronics and Telecommunication
Engineering**

in association with the
Institution's Innovation Council

Organizes

Poster Presentation of Ideas

Date : 23rd November 2023

Venue : Analog Electronics Lab (306)



Chair - IIC
Prof. Rakesh S. G.
Principal

President - IIC
Dr. Vanipriya Ch.
Professor & Head
Department of MCA

Vice President - IIC
Dr. Sasmita Mohapatra
Professor
Department of ECE

Convenor - IIC
Dr. Soumya Patil
Assoc. Professor & Head
Department of AI & ML